


[Web](#) [Images](#) [Groups](#) [News](#) [Froogle](#)<sup>New!</sup> [more »](#)

erik ruf

Search

[Advanced Search](#)  
[Preferences](#)
**Web**

Results 11 - 20 of about 7,170 for erik ruf. (0.27 seconds)

[PDF] [Todd B Knoblock and Erik Ruf Microsoft Research One Microsoft Way ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

D a t a Sp ecia lizat io n Todd B Knoblock and **Erik Ruf** Microsoft Research One Microsoft Way Redmond WA USA f toddk erikruf g microsoft com February Technical ...

[www.soften.ktu.lt/~damarobe/T120M013/Ld3/papers/mstr9604.pdf](http://www.soften.ktu.lt/~damarobe/T120M013/Ld3/papers/mstr9604.pdf) - [Similar pages](#)

### [functional programming archive \(partial evaluation\)](#)

... **Erik Ruf**, and Daniel Weise "Improving the accuracy of higher-order specialization using control flow analysis" In Proceedings of the ACM SIGPLAN Workshop on ...

[nick.dcs.qmul.ac.uk/SEL-HPC/Articles/GeneratedHtml/functional.partial.html](http://nick.dcs.qmul.ac.uk/SEL-HPC/Articles/GeneratedHtml/functional.partial.html) - 36k - [Cached](#) - [Similar pages](#)

### [Partial Evaluation](#)

... 1993. **Erik Ruf** and Daniel Weise. "On the Specialization of Online Program Specializers". ... Available online: ps. **Erik Ruf**. "Topics in Online Partial Evaluation". ...

[library.readscheme.org/page10.html](http://library.readscheme.org/page10.html) - 33k - [Cached](#) - [Similar pages](#)

### [DBLP: Daniel Weise](#)

... 14, **Erik Ruf**, Daniel Weise: On the Specialization of Online Program Specializers. ... 9,

**Erik Ruf**, Daniel Weise: Using Types to Avoid Redundant Specialization. ...

[www.informatik.uni-trier.de/~ley/db/indices/a-tree/w/Weise:Daniel.html](http://www.informatik.uni-trier.de/~ley/db/indices/a-tree/w/Weise:Daniel.html) - 8k - [Cached](#) - [Similar pages](#)

[ [More results from www.informatik.uni-trier.de](#) ]

### [Autoren - \[ Translate this page \]](#)

... Rüegg, Arthur: Mineralfarben. Rüttener, **Erik**: Erdbebengefährdung und Mikrozonierung in der Schweiz. **Ruf**, Claudia: Wieviel Tierschutz erlaubt das GATT? ...

[www.vdf.ethz.ch/autoren/r.html](http://www.vdf.ethz.ch/autoren/r.html) - 24k - [Cached](#) - [Similar pages](#)

### [From petersc@stud.ntnu.no Mon, 03 Apr 2000 11:00:11 +0200 Date ...](#)

... b/bolmstea/quines/"><http://www.mines.edu/students/b/bolmstea/quines/></a></a></a></a>

o. Partial Evaluation "I've been told to try reading **Erik Ruf's** PhD thesis ...

[lists.tunes.org/archives/review/2000-April.txt](http://lists.tunes.org/archives/review/2000-April.txt) - 43k - [Cached](#) - [Similar pages](#)

### [tunes-rev-refl 404s and fixes](#)

... edu/students/b/bolmstea/quines/ to: <http://www.mines.edu/students/b/bolmstea/quines/>

o. Partial Evaluation "I've been told to try reading **Erik Ruf's** PhD thesis ...

[lists.tunes.org/archives/review/2000-April/000063.html](http://lists.tunes.org/archives/review/2000-April/000063.html) - 7k - [Cached](#) - [Similar pages](#)

### [Teknologisk Institut - Erhvervs- og virksomhedsudvikling - RUF ...](#)

... [www.ruf.dk](http://www.ruf.dk) Opfindelser & Kreativitet, e-mail: Rasmus B. Offersen, tlf. 72 20 27

62 Case udarbejdet: august 2001. For yderligere information kontakt Hans **Erik** ...

[www.teknologisk.dk/erhverv/7709](http://www.teknologisk.dk/erhverv/7709) - 24k - Apr 1, 2004 - [Cached](#) - [Similar pages](#)

### [Re: re Ruf 5 speed](#)

... In Reply to: re **Ruf** 5 speed posted by **Erik** Gielen on February 21, 2003 at 12:14:54:

Hi **Erik**,. The WEVO 930 shifter will work fine in your application. ...

[wevo.com/wwwboard/messages/18.html](http://wevo.com/wwwboard/messages/18.html) - 4k - [Cached](#) - [Similar pages](#)

### [erik chang's home](#)

The summary for this Chinese (Traditional) page contains characters that cannot be correctly displayed in this language/character set.

[www.ruf.rice.edu/~auda/academic/](http://www.ruf.rice.edu/~auda/academic/) - 1k - [Cached](#) - [Similar pages](#)


[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☐ The ACM Digital Library ☒ The Guide

+"virtual function table" +optimization

SEARCH

## THE GUIDE TO COMPUTING LITERATURE

[Feedback](#) [Report a problem](#) [Satisfaction survey](#)
Terms used virtual function table optimizationFound **52** of **798,953**

Sort results by

relevance

Display results

expanded form

[Save results to a Binder](#) [Search Tips](#)☐ Open results in a new windowTry an [Advanced Search](#)Try this search in [The Digital Library](#)

Results 1 - 20 of 52

Result page: [1](#) [2](#) [3](#) [next](#)Relevance scale ☐ ☐ ☐ ☐ ☐

- 1 [Efficient dynamic dispatch without virtual function tables: the SmallEiffel compiler](#)  
 Olivier Zendra, Dominique Colnet, Suzanne Collin  
 October 1997 **ACM SIGPLAN Notices , Proceedings of the 12th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications**, Volume 32 Issue 10

Full text available: pdf(2.10 MB)

Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

SmallEiffel is an Eiffel compiler which uses a fast simple type inference mechanism to remove most late binding calls, replacing them by static bindings. Starting from the system's entry point, it compiles only statically living code, which saves compiling and then removing dead code. As the whole system is analyzed at compile time, multiple inheritance and genericity do not cause any overhead. SmallEiffel features a coding scheme which eliminates the need for virtual function tables. Dynamic dis ...

- 2 [The Jalapeño dynamic optimizing compiler for Java](#)  
 Michael G. Burke, Jong-Deok Choi, Stephen Fink, David Grove, Michael Hind, Vivek Sarkar, Mauricio J. Serrano, V. C. Sreedhar, Harini Srinivasan, John Whaley  
 June 1999 **Proceedings of the ACM 1999 conference on Java Grande**

Full text available: pdf(1.34 MB)

Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

- 3 [Implementing signatures for C++](#)  
 Gerald Baumgartner, Vincent F. Russo  
 January 1997 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 19 Issue 1

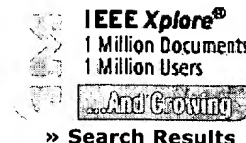
Full text available: pdf(305.82 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#), [review](#)

We outline the design and detail the implementation of a language extension for abstracting types and for decoupling subtyping and inheritance in C++. This extension gives the user more of the flexibility of dynamic typing while retaining the efficiency and security of static typing. After a brief discussion of syntax and semantics of this language extension and examples of its use, we present and analyze three different implementation techniques: a preprocessor to a C++ compiler, an implem ...

**Keywords:** C++, dispatch tables, inheritance, object interfaces, polymorphism, subtyping

- 4 [The direct cost of virtual function calls in C++](#)

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) | [Publications/Services](#) | [Standards](#) | [Conferences](#) | [Careers/Jobs](#)**IEEE Xplore®**  
RELEASE 1.6Welcome  
United States Patent and Trademark Office[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)[Quick Links](#)» [Search Results](#)**Welcome to IEEE Xplore®**

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

**Tables of Contents**

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

**Search**

- ☐ By Author
- ☐ Basic
- ☐ Advanced

**Member Services**

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **0** of **1015452** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard**Results:****No documents matched your query.**[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

**IEEE Xplore®**  
RELEASE 1.6Welcome  
United States Patent and Trademark Office[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)[Quick Links](#)**Welcome to IEEE Xplore®**

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

**Tables of Contents**

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

**Search**

- ☐ By Author
- ☐ Basic
- ☐ Advanced

**Member Services**

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **0** of **1015452** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard**Results:****No documents matched your query.**[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved

[IEEE HOME](#) | [SEARCH IEEE](#) | [SHOP](#) | [WEB ACCOUNT](#) | [CONTACT IEEE](#)[Membership](#) | [Publications/Services](#) | [Standards](#) | [Conferences](#) | [Careers/Jobs](#)**IEEE Xplore®**  
RELEASE 1.6Welcome  
United States Patent and Trademark Office  
**IEEE Xplore®**  
1 Million Documents  
1 Million Users  
And Growing» [Search Results](#)[Help](#) | [FAQ](#) | [Terms](#) | [IEEE Peer Review](#)[Quick Links](#)**Welcome to IEEE Xplore®**

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

**Tables of Contents**

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

**Search**

- ☐ By Author
- ☐ Basic
- ☐ Advanced

**Member Services**

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Your search matched **0** of **1015452** documents.A maximum of **500** results are displayed, **15** to a page, sorted by **Relevance** in **Descending** order.**Refine This Search:**

You may refine your search by editing the current search expression or entering a new one in the text box.

☐ Check to search within this result set**Results Key:****JNL** = Journal or Magazine   **CNF** = Conference   **STD** = Standard**Results:****No documents matched your query.**[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#) | [Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) | [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2004 IEEE — All rights reserved



US Patent &amp; Trademark Office

[Subscribe \(Full Service\)](#) [Register \(Limited Service, Free\)](#) [Login](#)

 Search: ☐ The ACM Digital Library ☒ The Guide

+"virtual function" +optimization

SEARCH

## THE GUIDE TO COMPUTING LITERATURE

[Feedback](#) [Report a problem](#) [Satisfaction survey](#)
Terms used virtual function optimization

Found 279 of 279

Sort results by

relevance

[Save results to a Binder](#)[Try an Advanced Search](#)

Display results

expanded form

[Search Tips](#)[Try this search in The Digital Library](#)
☐ Open results in a new window

Results 1 - 20 of 200

Result page: [1](#) [2](#) [3](#) [4](#) [5](#) [6](#) [7](#) [8](#) [9](#) [10](#) [next](#)

Best 200 shown

Relevance scale ☐ ☐ ☐ ☐ ☐1 [An enabling optimization for C++ virtual functions](#)

Bradley M. Kuhn, David W. Binkley

February 1996 **Proceedings of the 1996 ACM symposium on Applied Computing**Full text available: pdf(655.81 KB) Additional Information: [full citation](#), [references](#), [index terms](#)**Keywords:** dynamic finding, optimization, virtual functions2 [Fast static analysis of C++ virtual function calls](#)

David F. Bacon, Peter F. Sweeney

October 1996 **ACM SIGPLAN Notices , Proceedings of the 11th ACM SIGPLAN conference on Object-oriented programming, systems, languages, and applications**, Volume 31 Issue 10Full text available: pdf(2.10 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Virtual functions make code easier for programmers to reuse but also make it harder for compilers to analyze. We investigate the ability of three static analysis algorithms to improve C++ programs by resolving virtual function calls, thereby reducing compiled code size and reducing program complexity so as to improve both human and automated program understanding and analysis. In measurements of seven programs of significant size (5000 to 20000 lines of code each) we found that on average the mo ...

3 [Programming language optimizations for modular router configurations](#)

Eddie Kohler, Robert Morris, Benjie Chen

October 2002 **Tenth international conference on architectural support for programming languages and operating systems on Proceedings of the 10th international conference on architectural support for programming languages and operating systems (ASPLOS-X)**, Volume 36 , 30 , 37 Issue 5 , 5 , 10Full text available: pdf(1.31 MB) Additional Information: [full citation](#), [abstract](#), [references](#)

Networking systems such as Ensemble, the x-kernel, Scout, and Click achieve flexibility by building routers and other packet processors from modular components. Unfortunately, component designs are often slower than purpose-built code, and routers in particular have stringent efficiency requirements. This paper addresses the efficiency problems of one component-based router, Click, through optimization tools inspired in part by compiler optimization passes. This pragmatic approach can res ...

4 [Vortex: an optimizing compiler for object-oriented languages](#)